

University of Mumbai
Online Examination 2020

These are sample MCQs to indicate pattern, may or may not appeared in examination

Program: BE Electronics and Telecommunication Engineering

Curriculum Scheme: Revised 2016

Examination: Third Year Semester VI

Course Code: ECC 601 and Course Name: Microcontroller and Applications

Time: 1hour

Max. Marks: 50

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Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	Microcontroller 8051 has ____ address lines and ____ data lines
Option A:	16,8
Option B:	8,16
Option C:	20,16
Option D:	20,8
Q2.	LPC2148 ARM7 Microcontroller has two _____ timers/counters
Option A:	64-bit
Option B:	16-bit
Option C:	8-bit
Option D:	32-bit
Q3.	In ARM7, Abort mode generally enters when _____
Option A:	low priority interrupt is raised
Option B:	an attempt to access memory fails
Option C:	ARM processor is on rest
Option D:	undefined instructions are to be handled
Q4.	_____ is used to enables use of new PWM Match values
Option A:	PWM Match Control Register
Option B:	PWM Interrupt Register
Option C:	PWM Latch Enable Register
Option D:	PWM Control Register
Q5.	_____ is a vector address of IEO
Option A:	0003H
Option B:	000BH
Option C:	0013H
Option D:	001BH
Q6.	If status of T flip flop in CPSR register is set (T = 1)

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Option A:	ARM instruction set selected
Option B:	Thumb instruction set selected
Option C:	Jazelle instruction set selected
Option D:	data instruction set selected
Q7.	For LJMP instruction address size is _____
Option A:	8-bit
Option B:	11-bit
Option C:	16-bit
Option D:	13-bit
Q8.	In LCD, which hex command performs the function of 'Display on, cursor on and blinking'?
Option A:	0x0A
Option B:	0x0C
Option C:	0x0E
Option D:	0x0F
Q9.	Status of Z flag after the execution of CMP instruction given below, when R0 = 13; R9 = 12; is CMP R0, R9
Option A:	Z = 1
Option B:	Z = 0
Option C:	Same as previous value
Option D:	Z= Infinite
Q10.	Find out the roll over value for the timer in Mode 0, Mode 1 and Mode 2?
Option A:	00FFH,0FFFH,FFFFH
Option B:	1FFFH,0FFFH,FFFFH
Option C:	1FFFH,FFFFH,00FFH
Option D:	1FFFH,00FFH,FFFFH
Q11.	CPSR stands for _____
Option A:	Current program status register
Option B:	Current process status register
Option C:	Continued program status register
Option D:	Continued process status register
Q12.	Bit-addressable memory locations are:
Option A:	10H through 1FH
Option B:	20H through 2FH
Option C:	30H through 3FH
Option D:	40H through 4FH
Q13.	After Executing DIV AB instruction

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Option A:	Quotient is stored in A and Remainder in B
Option B:	Quotient is stored in B and Remainder in A
Option C:	Quotient is stored in R0 and Remainder in R1
Option D:	Quotient is stored in R0 and Remainder in A
Q14.	Which diodes are employed in the electromechanical relays since the inductor current cannot be reduced to zero?
Option A:	Tunnel Diode
Option B:	Shockley Diode
Option C:	Freewheeling Diode
Option D:	Zener Diode
Q15.	The bits that control the external interrupts of 8051 are
Option A:	ET0 and ET1
Option B:	ET1 and ET2
Option C:	EX0 and EX1
Option D:	EX1 and EX2
Q16.	___ Register is used as the Program Counter in ARM7.
Option A:	r13
Option B:	r14
Option C:	r15
Option D:	r16
Q17.	How many rows and columns are present in a 16*2 alphanumeric LCD?
Option A:	rows=2, columns=32
Option B:	rows=16, columns=2
Option C:	rows=16, columns=16
Option D:	rows=2, columns=16
Q18.	The I/O ports that are used as address and data for external memory are:
Option A:	ports 1 and 2
Option B:	ports 1 and 3
Option C:	ports 0 and 2
Option D:	ports 0 and 3
Q19.	MOVX A, @ R1 will:
Option A:	copy R1 to the accumulator
Option B:	copy the accumulator to R1
Option C:	copy the contents of external memory whose address is in R1 to the accumulator
Option D:	copy the accumulator contents to external memory whose address is in R1
Q20.	To generate delay of 100msec using ARM7 LPC2148 with PCLK of 30 MHz, Timer0 Match Register has to be loaded with_____ value

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Option A:	10000
Option B:	100
Option C:	10
Option D:	100000
Q21.	Which among the following data processing instruction use the barrel shifter?
Option A:	ADD R2, R5, R4
Option B:	MOV R5, R4, LSL #2
Option C:	MOV r5, R4,
Option D:	SUB R2, R5, R4
Q22.	What is the operation for timer mode 2 in Microcontroller 8051?
Option A:	13-bit timer mode
Option B:	16-bit timer mode
Option C:	8-bit auto reload mode
Option D:	Spilt timer mode
Q23.	Instruction used to multiply R3 contents by R2 and to store the result into R4 is called _____.
Option A:	MUL R4, R3, LSL #2
Option B:	MUL R4, R3, R2
Option C:	MUL R2, R3, LSR #2
Option D:	MUL R3, R4, R2
Q24.	The contents of the accumulator after executing following program MOV A,#2BH ORL A,00H will be
Option A:	1B H
Option B:	2B H
Option C:	3B H
Option D:	00 H
Q25.	An instruction that is used to move data from an ARM Register to a Status Register (CPSR or SPSR) is called _____.
Option A:	MRC
Option B:	MRS
Option C:	MSR
Option D:	MCS